

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	9	((("6,721,271") or ("6,667,956") or ("6,646,986") or ("6,580,721") or ("6,404,735") or ("6,393,590") or ("6,356,546") or ("6,088,331") or ("6,041,040")). PN.	US-PGPUB; USPAT	OR	OFF	2006/07/28 09:23
L2	50	((bus controller) with (arbitrat\$3) same priority) same ((count\$2 value weight\$3) near3 (increment\$3 decrement\$3 increas\$3 decreas\$3 add\$3 subtract\$3)) same (interval cycle period)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L3	539	(718/103).CCLS.	US-PGPUB; USPAT	OR	OFF	2006/07/28 10:34
L4	1129	(718/104).CCLS.	US-PGPUB; USPAT	OR	OFF	2006/07/28 10:34
L5	494	(access\$3 with priority with (process thread task) with (memory))	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L6	28	(access\$3 with priority with (process thread task) with counter)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L7	1992	(access\$3 with priority with (process thread task))	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L8	34	(access\$3 with priority with (process thread task)) same (increment\$6 decrement\$6)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L9	34	(access\$3 with priority with (process thread task)) same (increment\$6 decrement\$6)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L10	28	(access\$3 with priority with counter with (process thread task))	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L11	412	(access\$3 with priority with counter)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L12	27	(arbitrat\$3) same priority same ((count\$2 value weight\$3) near3 (increment\$3 decrement\$3 increas\$3 decreas\$3 add\$3 subtract\$3)) same (interval cycle period) same (frequen\$4 rate)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34

## EAST Search History

L13	25	(process thread task) with (access\$3) near (resource memory) with (counter value variable) with (decreas\$3 subtract\$3 decrement\$6)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L14	62	(process thread task) with (access\$3) near (resource memory) with (counter value variable) with (increment\$6 decrement\$6)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L15	4	(process thread task) with (access\$3) near2 (resource memory) with (bandwidth) with (decreas\$3 subtract\$3 decrement\$6)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L16	30	(process thread task) with (access\$3) near2 (resource memory) with (counter value variable) with (decreas\$3 subtract\$3 decrement\$6)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L17	347	(process thread task) with (access\$3) with (counter value variable) with (increment\$6 decrement\$6)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L18	190	(thread process task) same priority same ((count\$2 value weight\$3 variable) near3 (decrement\$3 decreas\$3 subtract\$3)) same (frequen\$4 rate)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L19	190	(thread process task) same priority same ((count\$2 value weight\$3 variable) near3 (decrement\$3 decreas\$3 subtract\$3)) same (frequen\$4 rate)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L20	0	(thread process task) with access\$3 adj3 (memory resource) same priority same ((count\$2 value weight\$3 variable) near3 (decrement\$3 decreas\$3 subtract\$3)) same (frequen\$4 rate)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L21	0	(thread process task) with access\$3 same priority same ((count\$2 value weight\$3 variable) near3 (decrement\$3 decreas\$3 subtract\$3)) same (frequen\$4 rate)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L22	6	(thread process task) with priority with ((count\$2 value weight\$3 variable) near3 (decrement\$3 decreas\$3 subtract\$3)) same (frequen\$4 rate)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L23	319	(thread process task) with priority with (count\$2 value weight\$3 variable) same (frequen\$4 rate)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34

## EAST Search History

L24	11	(thread process task) with priority with (count\$2 value weight\$3 variable) with (decrement\$4 decreas\$3 subtract\$3) same (frequen\$4 rate)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L25	355	(thread process task) with schedul\$4 with (counter)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L26	220	(thread process) with (burst time adj slice quantum) with (decrement\$4 decreas\$3 subtract\$3)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L27	7	(thread process) with (burst time adj slice quantum) with (decrement\$4 decreas\$3 subtract\$3) near2 counter	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L28	0	(thread process) with (burst time adj slice quantum) with (decrement\$4 decreas\$3 subtract\$3) same arbitration	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L29	32	(thread process) with (burst time adj slice quantum) with (decrement\$4 decreas\$3 subtract\$3) with (variable value counter)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L30	14	(thread process) with (burst time adj slice quantum) with (decrement\$4 decreas\$3 subtract\$3) with (variable value counter) and priority	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L31	0	(thread process) with (burst time adj slice quantum) with (decrement\$4 decreas\$3 subtract\$3) with arbitration	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L32	8	(thread process) with (burst time adj slice quantum) with (decrement\$4 decreas\$3 subtract\$3) with counter	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L33	6	(thread process) with schedul\$4 with (burst time adj slice quantum) with (decrement\$4 decreas\$3 subtract\$3)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L34	8	(US-6684280-\$ or US-5884051-\$ or US-5339443-\$ or US-5935234-\$ or US-5193197-\$ or US-6363445-\$ or US-6154769-\$).did. or (US-20020143843-\$).did.	US-PGPUB; USPAT	OR	OFF	2006/07/28 10:34

## EAST Search History

L35	982	access adj counter	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L36	154	access adj counter same (process thread)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L37	44	access adj counter same (process thread) same (increment\$4 decrement\$4)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L38	282	access adj cycle same priority	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L39	4	access adj cycle same priority same (counter value) near2 access\$3	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L40	41	count\$2 with (increment\$3 decrement\$3 increas\$3 decreas\$3 add\$3 subtract\$3) and priority same (shar\$3 near2 (resource memory computer)) same (access\$3 near3 (interval cycle period))	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L41	1091	memory adj controller with arbitrat\$3	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L42	3	memory adj controller with arbitrat\$3 with (decrement\$3 decreas\$3 subtract\$3)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L43	0	memory adj controller with arbitrat\$3 with (decrement\$3 decreas\$3 subtract\$3) near2 counter	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L44	0	memory adj controller with arbitrat\$3 with (decrement\$3 decreas\$3 subtract\$3) with counter	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L45	131	memory adj controller with arbitrat\$3 with priority	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34

## EAST Search History

L46	1	memory adj controller with arbitrat\$3 with priority with (thread process)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L47	1129	(718/104).CCLS.	US-PGPUB; USPAT	OR	OFF	2006/07/28 10:34
L48	0	L47 and (access\$3 with priority with counter)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L49	539	(718/103).CCLS.	US-PGPUB; USPAT	OR	OFF	2006/07/28 10:34
L50	4	L49 and (access\$3 with priority with counter)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L51	412	(access\$3 with priority with counter)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L52	291	L51 and (((@ad < "20010126") or (@prad < "20010126") or (@rlad < "20010126"))	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L53	236	L51 and (((@ad < "20010126"))	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L54	28	(access\$3 with priority with counter with (process thread task))	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L55	34	(access\$3 with priority with (process thread task)) same (increment\$6 decrement\$6)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L56	33	L55 not L54	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L57	494	(access\$3 with priority with (process thread task) with (memory))	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34

## EAST Search History

L58	362	L57 and (((@ad < "20010126") or (@prad < "20010126") or (@rlad < "20010126"))	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L59	308	L57 and (((@ad < "20010126"))	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L60	62	(process thread task) with (access\$3) near (resource memory) with (counter value variable) with (increment\$6 decrement\$6)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L61	31	L60 and (((@ad < "20010126"))	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L62	25	(process thread task) with (access\$3) near (resource memory) with (counter value variable) with (decreas\$3 subtract\$3 decrement\$6)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L63	14	L62 and (((@ad < "20010126"))	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L64	30	(process thread task) with (access\$3) near2 (resource memory) with (counter value variable) with (decreas\$3 subtract\$3 decrement\$6)	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L65	15	L64 and (((@ad < "20010126"))	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L66	14	L62 and (((@ad < "20010126"))	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L67	15	L64 and (((@ad < "20010126"))	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34
L68	1	L67 not L66	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 10:34

## EAST Search History

L75	23	(access\$3 same cycle same priority same resource).clm.	US-PGPUB; USPAT; USOCR; EPO; DERWENT ; IBM_TDB	OR	ON	2006/07/28 11:08
-----	----	---	---	----	----	------------------

[Web](#) [Images](#) [Groups](#) [News](#) [Froogle](#) [Maps](#) [more »](#)

site:portal.acm.org priority counter arbitration access cycle memory shared

Search

[Advanced Search](#)  
[Preferences](#)

Web

Results 1 - 84 of about 101 from portal.acm.org for priority counter arbitration access cycle memory shared. (0.61 seconds)

[\[PDF\] TRANSPORT PROTOCOL PROCESSING AT GBPS RATES](#)

File Format: PDF/Adobe Acrobat

arbitration unit. To start a bus cycle, the request line is asserted. ... reduce shared memory access in several ways. The first time a ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=99551&  
type=pdf&coll=ACM&dl=ACM&CFID=15151515&... - [Similar pages](#)

[\[PDF\] The Parallel Protocol Engine](#)

File Format: PDF/Adobe Acrobat

of the same priority merely. requires placing the program counter on the stack and ... match it to the appropriate. access cycle of the PPE shared. memory. ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=174380&  
type=pdf&coll=ACM&dl=ACM&CFID=15151515... - [Similar pages](#)

[\[PDF\] Concert: Design of a Multiprocessor Development System ...](#)

File Format: PDF/Adobe Acrobat

Non-uniform memory access times. The cost of Concert has ... priority order. Indeed, we are studying other arbitration algorithms, ...  
portal.acm.org/ft\_gateway.cfm?id=17361&  
type=pdf&coll=GUIDE&dl=GUIDE&CFID=15151515&CFT... - [Similar pages](#)

[\[PDF\] ntroducing a new, 64-bit computer architecture posed a number of ...](#)

File Format: PDF/Adobe Acrobat

arbitration cycle. Initially, each initiator's priority is set to its slot number. ... memory. The other memory is. shared between use as command reg- ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=151225&  
type=pdf&dl=ACM&dl=ACM&CFID=15151515&a... - [Similar pages](#)

[\[PDF\] Design and Evaluation of A DRAM-based Shared Memory ATM Switch](#)

File Format: PDF/Adobe Acrobat

count through a priority counter to maintain. a rotating ... uga, access requests from an input port to the shared. buffer memory never block because any ...  
portal.acm.org/ft\_gateway.cfm?id=258693&  
type=pdf&coll=ACM&dl=ACM&CFID=15151515&CFTOKE... - [Similar pages](#)

[\[PDF\] Concert: Design of a Multiprocessor Development System ...](#)

File Format: PDF/Adobe Acrobat

Use of shared memory as the principal medium for interprocessor. communication. ... priority order. Indeed, we are studying other arbitration algorithms, ...  
portal.acm.org/ft\_gateway.cfm?id=17361&  
type=pdf&coll=portal&dl=ACM&CFID=15151515&CFTO... - [Similar pages](#)

[\[PDF\] costlperformance multiprocessors asynchronous multiprocessor is ...](#)

File Format: PDF/Adobe Acrobat

bus arbitration discipline [JOR74]. Although a fixed-priority ... memory having about 300 ns access time and 500 ns read cycle time. ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=803546&type=pdf&coll=ACM&dl=ACM - [Similar pages](#)

[\[PDF\] Multicast Contention Resolution with Single-Cycle Windowing Using ...](#)

File Format: PDF/Adobe Acrobat

been granted access for a queued cell, it performs no other. checks for the remainder of the arbitration cycle, since each ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=241065&  
type=pdf&coll=ACM&dl=guide&CFID=151515... - [Similar pages](#)

[\[PDF\] OMP: A RISC-based Multiprocessor using Orthogonal-Access Memories ...](#)

File Format: PDF/Adobe Acrobat

Since the arbitration. is per- formed once every orthogonal memory cycle (the time. to transfer 16 data elements), the access control board ...  
portal.acm.org/ft\_gateway.cfm?id=255133&  
type=pdf&coll=portal&dl=ACM&CFID=15151515&CFT... - [Similar pages](#)

[\[PDF\] Using to Reduce Memory Bank Contention for Decoupled Operand ...](#)

File Format: PDF/Adobe Acrobat

cycle,. it was necessary. to establish. a. priority. for these. transfers. ... interconnection. in the memory. path. A distributed. arbitration ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=125938&  
type=pdf&coll=ACM&dl=ACM&CFID=1222393&... - [Similar pages](#)



[PDF] THE DESIGN PARALLEL PROCESSOR FOR IMAGE PROCESSING ON-BOARD ...

File Format: PDF/Adobe Acrobat

nication needs come from **shared access** to this ... **cycle** basis then **access** to this **memory** will be ... well as **priority** request signal. They are arran- ...

portal.acm.org/ft\_gateway.cfm?id=801677&amp;

type=pdf&coll=portal&dl=ACM&CFID=15151515&CFT... - [Similar pages](#)[PDF] JMTP: An Architecture for Exploiting Concurrency in Embedded Java ...

File Format: PDF/Adobe Acrobat

**counter** is thus decremented each **cycle**. This mode is suitable ... is due to **arbitration** on. **shared** resources such as **access** to **memory** and monitors. This ...

portal.acm.org/ft\_gateway.cfm?id=340076&amp;

type=pdf&coll=portal&dl=ACM&CFID=15151515&CFT... - [Similar pages](#)[PDF] AND THE ELI-512 JOSEPH A. FISHER

File Format: PDF/Adobe Acrobat

**cycle**, we **access** each of the **memory** banks' address registers. individually. No **arbitration** is necessary, since the paths of the. addresses will never cross. ...

portal.acm.org/ft\_gateway.cfm?id=801649&amp;

type=pdf&coll=portal&dl=ACM&CFID=15151515&CFT... - [Similar pages](#)[PDF] Instruction Fetch Mechanisms for Multipath Execution Processors

File Format: PDF/Adobe Acrobat

cution engine and the issues that arise with respect to **shared** ... following components: (1) path PC **arbitration** to gain **access**. to the instruction cache, ...

portal.acm.org/ft\_gateway.cfm?id=320087&amp;

type=pdf&coll=GUIDE&dl=GUIDE&CFID=15151515&CF... - [Similar pages](#)[PDF] A New Switch Chip for IBM RS/6000 SP Systems

File Format: PDF/Adobe Acrobat

idle, it has a higher **priority** in this **arbitration** than a busy output port. ... P. Vatsolaki, and A. Efthymiou, "Pipelined **Memory Shared** Buffer for VLSI ...

portal.acm.org/ft\_gateway.cfm?id=331548&type=pdf - [Similar pages](#)[PDF] Fast and Fair: Data-stream Quality of Service

File Format: PDF/Adobe Acrobat

sources with little interaction and coordinated **arbitration**. ... 1 port, sequential **access**. **Shared** L2. 4MB, 32-way, 64B blocks, 51 **cycle** hit, LRU ...

portal.acm.org/ft\_gateway.cfm?id=1086328&amp;

type=pdf&coll=GUIDE&dl=ACM&CFID=15151515&CFT... - [Similar pages](#)[PDF] A VLIW Architecture for a Trace Scheduling Compiler

File Format: PDF/Adobe Acrobat

**access** time of the dynamic RAMs used to build the **memory**. The **memory** architecture of ... into a single execution **cycle**, we must establish a **priority** rela- ...

portal.acm.org/affiliated/ft\_gateway.cfm?id=36201&amp;

type=pdf&coll=portal&dl=acm&CFID=151515... - [Similar pages](#)[PDF] Synchronizing processors through memory requests in a tightly ...

File Format: PDF/Adobe Acrobat

with a **shared memory** through a GREEDY network and synchronized ... **memory**. **cycle** and the data on which this **arbitration** must be performed ...

portal.acm.org/affiliated/ft\_gateway.cfm?id=52444&amp;type=pdf&amp;coll=GUIDE&amp;dl=ACM -

[Similar pages](#)[PDF] Conference in Research and Practice in Information Technology ...

File Format: PDF/Adobe Acrobat

register file with multi-**cycle** **access** time would increase. branch misprediction penalty since a ... implement synchronous counters and **priority** **arbitration**. ...

portal.acm.org/affiliated/ft\_gateway.cfm?id=979930&amp;

type=pdf&coll=ACM&dl=acm&CFID=15151515... - [Similar pages](#)[PDF] Abstract

File Format: PDF/Adobe Acrobat

having top **priority**. With smart **arbitration** the round. robin will not "advance" if, for example, ... **Shared Memory** Parallel Computer,". IEEE Tranractions ...

portal.acm.org/affiliated/ft\_gateway.cfm?id=52439&amp;

type=pdf&coll=ACM&dl=ACM&CFID=800494&am... - [Similar pages](#)[PDF] CACHE MEMORY PERFORMANCE IN A UNIX ENVIRONMENT Cedell Alexander ...

File Format: PDF/Adobe Acrobat

pipeline stages, the average **memory** **cycle** time degradation equals the ... as follows: when a processor requests **shared** **access** to a block (privilege to ...

portal.acm.org/affiliated/ft\_gateway.cfm?id=381717&amp;type=pdf&amp;dl=GUIDE&amp;dl=ACM -

[Similar pages](#)[PDF] Distributed Round-Robin and First-Come First-Serve Protocols and ...

File Format: PDF/Adobe Acrobat

case, agents follow the assured **access** protocol for non-**priority**. requests, but ignore the protocol and compete in every **arbitration** for. **priority** requests. ...  
portal.acm.org/ft\_gateway.cfm?id=52431&type=pdf - [Similar pages](#)

[PDF] [A Practical Implementation of the Fault-Tolerant Daisy-Chain Clock ...](#)

File Format: PDF/Adobe Acrobat  
are usually connected by a **shared** serial media, the underlying communication platform is the ... **multiple access** (TDMA) **arbitration** scheme, where each ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=1131395&type=pdf&coll=ACM&dl=acm&CFID=1515151... - [Similar pages](#)

[PDF] [TCPivo: A High-Performance Packet Replay Engine](#)

File Format: PDF/Adobe Acrobat  
kernel patches and **priority** scheduling, TCPivo can be made. highly resilient to background ... **access** to the **shared memory**. Both can limit the maximum ...  
portal.acm.org/ft\_gateway.cfm?id=944783&type=pdf&coll=portal&dl=ACM&CFID=15151515&CFT... - [Similar pages](#)

[PDF] [THE MANCHESTER PROTOTYPE DATAFLOW COMPUTER -](#)

File Format: PDF/Adobe Acrobat  
their output onto the **arbitration** bus and thence out of. the module. ... The Instruction Store comprises a random-**access memory** and. two registers. ...  
portal.acm.org/citation.cfm?doid=2465.2468 - [Similar pages](#)

[PDF] [High-level architectural co-simulation using Esterel and C](#)

File Format: PDF/Adobe Acrobat  
(a) The fixed hardware **priority arbitration** scheme grants. peripheral bus **access** to the current ... **access** delay to be added to the bus **cycle** duration. ...  
portal.acm.org/ft\_gateway.cfm?id=371719&type=pdf&coll=GUIDE&dl=GUIDE&CFID=72743048&CF... - [Similar pages](#)

[PDF] [Design, Implementation, and Verification of Active Cache Emulator ...](#)

File Format: PDF/Adobe Acrobat  
injection to the FSB to lengthen (dilate) the host **memory access** ... During the **arbitration** phase the FSB protocol selects (among ...  
portal.acm.org/ft\_gateway.cfm?id=1117211&type=pdf&coll=ACM&dl=ACM&CFID=15151515&CFTOK... - [Similar pages](#)

[PDF] [SURVEY OF COMMERCIAL PARALLEL MACHINES](#)

File Format: PDF/Adobe Acrobat  
**shared memory** programming model is made possible by a patented new architectural technique ... board that handles **arbitration** and **access** to the bus. ...  
portal.acm.org/ft\_gateway.cfm?id=152837&type=pdf&coll=GUIDE&dl=GUIDE&CFID=76430868&CF... - [Similar pages](#)

[PDF] [Balancing Performance and Flexibility with Hardware Support for ...](#)

File Format: PDF/Adobe Acrobat  
Compared to using a **shared memory** pool, this implementation simplifies. the interface to the buffer and also eliminates the need for **arbitration** logic. ...  
portal.acm.org/citation.cfm?doid=945506.945508 - [Similar pages](#)

[PDF] [Boosting the Performance of Hybrid Snooping Cache Protocols](#)

File Format: PDF/Adobe Acrobat  
Eggers and Katz [6] as a sequence of write references to a **shared**. cache block by a single processor uninterrupted. by any **memory access** to the same ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=223998&type=pdf&dl=ACM&dl=ACM&CFID=15151515&a... - [Similar pages](#)

[PDF] [Complementary Use of Runtime Validation and Model Checking](#)

File Format: PDF/Adobe Acrobat  
token **arbitration** where only one unit can do a task at a time. ... tional Symposium on **Shared Memory** Multiprocessors. (sponsored by ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=1129750&type=pdf&coll=ACM&dl=acm&CFID=1217414... - [Similar pages](#)

[PDF] [Floorplan-Aware Automated Synthesis of Bus-based Communication ...](#)

File Format: PDF/Adobe Acrobat  
bus **cycle** time violations, while synthesizing a feasible, low-cost ... **arbitration** **priority** orderings, data bus widths, bus clock speeds and DMA ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=1065727&type=pdf&coll=ACM&dl=ACM&CFID=1515151... - [Similar pages](#)

[PDF] [TCPivo: A High-Performance Packet Replay Engine](#)

File Format: PDF/Adobe Acrobat  
**access** to the **shared memory**. Both can limit the maximum. replay throughput supported. ... **arbitration** and PCI/NIC buffering that can impact the ac- ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=944783&type=pdf&coll=ACM&dl=ACM&CFID=923434&a... - [Similar pages](#)

[PDF] This paper outlines the objectives and preliminary design for an ...

File Format: PDF/Adobe Acrobat

on busses with **priority arbitration** logic. 2'3 ... microprogrammable minicomputer, and **access**. to a large virtual **memory**. This **memory** can ...

portal.acm.org/affiliated/ft\_gateway.cfm?id=805677&amp;

type=pdf&coll=ACM&dl=ACM&CFID=15151515... - [Similar pages](#)[PDF] Y(s) G(s) X(s) (i)

File Format: PDF/Adobe Acrobat

The bus select and **arbitration** logic allows both processors to **access** a common **memory**(iKx16 bits RAM). Only one processor can **access** this common **memory** at a ...

portal.acm.org/affiliated/ft\_gateway.cfm?id=809932&amp;

type=pdf&coll=portal&dl=GUIDE&CFID=151... - [Similar pages](#)[PDF] THE GOULD NPI SYSTEM INTERCONNECTION

File Format: PDF/Adobe Acrobat

**memory** is in the **access** of semaphores protecting **shared** data ... functions of a bus controller such as bus **arbitration** and system. clock control. ...

portal.acm.org/ft\_gateway.cfm?id=55381&amp;

type=pdf&coll=GUIDE&dl=GUIDE&CFID=68597125&CFT... - [Similar pages](#)[PDF] MARK IIIfp HYPERCUBE CONCURRENT PROCESSOR ARCHITECTURE' J. TuazonI...

File Format: PDF/Adobe Acrobat

can **access** the **shared**. **memory** directly through the 32-bit data and ... **arbltration**. is managed by the MC68452. bus. **arbltration**. unit and the RS232 ...

portal.acm.org/ft\_gateway.cfm?id=62307&amp;

type=pdf&coll=portal&dl=ACM&CFID=15151515&CFTO... - [Similar pages](#)[PDF] Xunet 2: Lessons from an Early Wide-Area ATM Testbed - Networking ...

File Format: PDF/Adobe Acrobat

During each **arbitration** cycle, a queue handler that wishes to ... **Shared** **memory** was used for communication between parts. of the system. ...

portal.acm.org/affiliated/ft\_gateway.cfm?id=245225&amp;

type=pdf&coll=ACM&dl=acm&CFID=15151515... - [Similar pages](#)[PDF] Orion: A Power-Performance Simulator for Interconnection Networks ...

File Format: PDF/Adobe Acrobat

**memory** **shared** buffer for VLSI switches. In Proc. Confer- ... An enhanced **access** and **cycle**. time model for on-chip caches. Technical Report 93/5, DEC ...

portal.acm.org/ft\_gateway.cfm?id=774893&amp;

type=pdf&coll=GUIDE&dl=GUIDE&CFID=15151515&CF... - [Similar pages](#)[PDF] Modeling and Simulation of an i860-Based Multiprocessor

File Format: PDF/Adobe Acrobat

formance because it controls **access** to the **shared** bus. and **memory**. The arbiter simulations ... ers and software developers early in the design **cycle**. ...

portal.acm.org/affiliated/ft\_gateway.cfm?id=306846&amp;

type=pdf&coll=ACM&dl=ACM&CFID=15151515... - [Similar pages](#)[PDF] A Retrospective on the Dorado, A High-Performance Personal ...

File Format: PDF/Adobe Acrobat

nanosecond microinstruction **cycle** time, a high speed cache, **memory** ... and using a **shared** bus or a switch to **access** the **memory**, the Dorado ...

portal.acm.org/affiliated/ft\_gateway.cfm?id=801663&amp;type=pdf&amp;coll=ACM&amp;dl=ACM -

[Similar pages](#)[PDF] Random Testing of Interrupt-Driven Software

File Format: PDF/Adobe Acrobat

mits the processor's interrupt **arbitration** logic to determin- ... **shared** **memory** and network inputs that were computed us-. ing a genetic algorithm. ...

portal.acm.org/affiliated/ft\_gateway.cfm?id=1086282&amp;type=pdf&amp;coll=portal&amp;dl=ACM -

[Similar pages](#)[PDF] The Atomos Transactional Programming Language

File Format: PDF/Adobe Acrobat

scheduler thread via scheduler command queues in **shared** **memory** that interrupt the scheduler loop ... L2 hit time includes **arbitration** and bus transfer time. ...

portal.acm.org/affiliated/ft\_gateway.cfm?id=1133983&amp;

type=pdf&coll=ACM&dl=ACM&CFID=15151515... - [Similar pages](#)[PDF] Enhancing Software Reliability with Speculative Threads

File Format: PDF/Adobe Acrobat

two highest **priority** threads whose fetches are not blocked. We also prioritize the **arbitration** for **shared** resources each. **cycle** to favor the higher-**priority** ...

portal.acm.org/affiliated/ft\_gateway.cfm?id=605417&amp;

type=pdf&coll=ACM&dl=ACM&CFID=15151515... - [Similar pages](#)

**[PDF] THECONVEXC240ARCHITECTURE**

File Format: PDF/Adobe Acrobat

The **memory** system. supports. a tightly. coupled, **shared** processor structure. ... addresses, permitting single **cycle access**. Due to the high ...  
portal.acm.org/ft\_gateway.cfm?id=63014&  
type=pdf&coll=portal&dl=ACM&CFID=15151515&CFTO... - [Similar pages](#)

**[PDF] A Processor for a High-Performance Personal Computer**

File Format: PDF/Adobe Acrobat

During the second half **cycle**. 01-12), the. micro-. program address for the highest **priority** task is fetched. from the task specific program **counter** TPC. ...  
portal.acm.org/ft\_gateway.cfm?id=801920&  
type=pdf&coll=portal&dl=ACM&CFID=15151515&CFT... - [Similar pages](#)

**[PDF] A Stateless, Content-Directed Data Prefetching Mechanism**

File Format: PDF/Adobe Acrobat

have to endure a full L2 to main **memory** request **cycle**; the pointer ... each **memory** request, with this **priority** being used during **memory**. bus **arbitration**. ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=605427&  
type=pdf&coll=ACM&dl=ACM&CFID=15151515... - [Similar pages](#)

**[PDF] SELF-ASSESSMENT PROCEDURE XVI 8 i/ ( 1 a**

File Format: PDF/Adobe Acrobat

which of the following methods of **arbitration**. or **priority** resolution can resolve the **priority** at ... nificant lowering of the **memory** **access** time at ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=7543&type=pdf&coll=ACM&dl=ACM - [Similar pages](#)

**[PDF] Inferential Queueing and Speculative Push for Reducing Critical ...**

File Format: PDF/Adobe Acrobat

22ns snoop **cycle** (including 2 ns **arbitration**). pipelined, point-to-point crossbar ... approach to exclusive data **access** in **shared** **memory** multi-. processors. ...  
portal.acm.org/ft\_gateway.cfm?id=782853&type=pdf - [Similar pages](#)

**[PDF] Improving I/O Performance with a Conditional Store Buffer**

File Format: PDF/Adobe Acrobat

similar to the transactional **memory** proposed in [6]. It is a. general hardware mechanism to support lock free atomic. **access** to **shared** data structures. ...  
portal.acm.org/ft\_gateway.cfm?id=290975&  
type=pdf&coll=ACM&dl=GUIDE&CFID=15151515&CFTO... - [Similar pages](#)

**[PDF] RuleBase: an Industry-Oriented Formal Verification Tool**

File Format: PDF/Adobe Acrobat

standard hardware design **cycle**. In order to do so, the technology ... the second project, a distributed **shared-memory** MESI cache co- ...  
portal.acm.org/ft\_gateway.cfm?id=240642&  
type=pdf&coll=portal&dl=ACM&CFID=15151515&CFT... - [Similar pages](#)

**[PDF] RuleBase: an Industry-Oriented Formal Verification Tool**

File Format: PDF/Adobe Acrobat

RuleBase presents **counter**-examples and witnesses (see below) as. timing diagrams. ... the second project, a distributed **shared-memory** MESI cache co- ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=240642&  
type=pdf&coll=ACM&dl=ACM&CFID=290202&a... - [Similar pages](#)

**[PDF] Microarchitecture of a High-Radix Router**

File Format: PDF/Adobe Acrobat

of the network largely establish the remote **memory** **access**. latency and bandwidth. ... switch **arbitration** is done speculatively. Each **cycle**, each ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=1070005&type=pdf&coll=ACM&dl=ACM - [Similar pages](#)

**[PDF] Vector Prefetching**

File Format: PDF/Adobe Acrobat

**shared** by all **memory** modules (see figure 2). Bus **arbitration**. takes one **cycle**. Transmitting the request to a **memory** mod-. ule requires exclusive bus **access** ...  
portal.acm.org/ft\_gateway.cfm?id=218329&  
type=pdf&coll=ACM&dl=ACM&CFID=15151515&CFTOKE... - [Similar pages](#)

**[PDF] Evaluation of the Lock Mechanism in a Snooping Cache**

File Format: PDF/Adobe Acrobat

lock bits on the **shared** **memory**. and **access** them with atomic. test-and-set ... mined from the **access** pattern is 5%. Fixed-**priority**. bus **arbitration** ...  
portal.acm.org/ft\_gateway.cfm?id=143384&  
type=pdf&coll=GUIDE&dl=ACM&CFID=15151515&CFTO... - [Similar pages](#)

**[PDF] A simple and efficient bus management scheme that supports ...**

File Format: PDF/Adobe Acrobat

efficient. bandwidth. management. and. **access. arbitration** ... gives **priority**. to random. cells, when. in a service. **cycle**, the remaining ...  
portal.acm.org/ft\_gateway.cfm?id=201048&type=pdf - [Similar pages](#)

[PDF] [h Processor for a High-Performance Personal Computer](#)

File Format: PDF/Adobe Acrobat

controller and using a **shared** bus or a switch to **access** the. **memory** ... **priority** encoding and reading of TPC. into. one **cycle** is quite difficult. these two ...  
portal.acm.org/ft\_gateway.cfm?id=285978&  
type=pdf&coll=ACM&dl=ACM&CFID=15151515&CFTOKE... - [Similar pages](#)

[PDF] [Isochronets: an Architecture for High-Speed Networks](#)

File Format: PDF/Adobe Acrobat

**Memory**. b. Ethernet Line Card. Figure 10: Overall electronic design. Isochronet switches thus separate high-speed. transmission path and **access arbitration** ...  
portal.acm.org/ft\_gateway.cfm?id=962294&  
type=pdf&coll=GUIDE&dl=GUIDE&CFID=68605781&CF... - [Similar pages](#)

[PDF] [On-Chip Communication Architecture for OC-768 Network Processors](#)

File Format: PDF/Adobe Acrobat

rate corresponds to the **memory access** speed. We ignore all. propagation delays. ... that these **arbitration** policies do not affect the throughput ...  
portal.acm.org/ft\_gateway.cfm?id=379047&  
type=pdf&coll=ACM&dl=ACM&CFID=15151515&CFTOKE... - [Similar pages](#)

[PDF] [Experience Using Multiprocessor SystemsA Status Report](#)

File Format: PDF/Adobe Acrobat

Direct **Memory Access** (DMA) devices. transfer data directly into **shared memory**. ... are its **priority**, an integer value in the range ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=356813&type=pdf&coll=ACM&dl=ACM - [Similar pages](#)

[PDF] [An Architecture Level Simulation Methodology](#)

File Format: PDF/Adobe Acrobat

**shared** basis, with the IOC having the **priority**. After the. command instruction, the CP suspends its ... **memory** module has a fixed **cycle** time that depends on ...  
portal.acm.org/ft\_gateway.cfm?id=306866&  
type=pdf&coll=GUIDE&dl=GUIDE&CFID=15151515&CF... - [Similar pages](#)

[PDF] [Power Analysis of System-Level On-Chip Communication Architectures](#)

File Format: PDF/Adobe Acrobat

**access** to the **shared** bus using a configurable **arbitration** scheme. ... from masters, and selecting the one with the highest (fixed) **priority**. ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=1016777&type=pdf&coll=Portal&dl=ACM - [Similar pages](#)

[PDF] [Preventing Interrupt Overload](#)

File Format: PDF/Adobe Acrobat

tem by ensuring that **access** to each **shared** resource is mediated ... the former is logically outside the processor's interrupt **arbitration** ...  
portal.acm.org/ft\_gateway.cfm?id=1065918&  
type=pdf&coll=GUIDE&dl=GUIDE&CFID=713149&CFT... - [Similar pages](#)

[PDF] [The Use of Message-Based Multicomputer Components to Construct ...](#)

File Format: PDF/Adobe Acrobat

KBytes of external dual-**access memory** for program, data. and message storage . ... the source node's **shared memory**, over channels and back ...  
portal.acm.org/affiliated/ft\_gateway.cfm?id=174201&  
type=pdf&coll=ACM&dl=ACM&CFID=15151515... - [Similar pages](#)

[PDF] [COMPUTER COMMUNICATION TECHNIQUES Kenneth J. Thurber Sperry Univac...](#)

File Format: PDF/Adobe Acrobat

**shared** and the system topology. The resultant taxonomy is shown ... nection at any point on the bus requires that the **arbitration** func- ...  
portal.acm.org/ft\_gateway.cfm?id=859428&  
type=pdf&coll=portal&dl=ACM&CFID=15151515&CFT... - [Similar pages](#)

[PDF] [THE HONEYWELL MODULAR MICROPROGRAM MACHINE: M 3. by E. Douglas...](#)

File Format: PDF/Adobe Acrobat

the same **priority** level. After **arbitration** is completed, the unit awarded the. next bus **cycle** initiates its usage. The Bus Release line is ...  
portal.acm.org/ft\_gateway.cfm?id=810648&  
type=pdf&coll=ACM&dl=ACM&CFID=15151515&CFTOKE... - [Similar pages](#)

[PDF] [Piranha: A Scalable Architecture Based on Single-Chip Multiprocessing](#)

File Format: PDF/Adobe Acrobat

power **cycle** safely, persistent **memory** requires mechanisms to. force volatile (cached) state to safe **memory**, as well as mecha-. nisms to control **access** to ...  
portal.acm.org/ft\_gateway.cfm?id=339696&

type=pdf&coll=portal&dl=ACM&CFID=15151515&CFT... - [Similar pages](#)

[PDF] [An Extension to the SCI Flow Control Protocol for Increased ...](#)

File Format: PDF/Adobe Acrobat

more than their fair **share** of. bandwidth. As a **counter** measure, rule E5 ) allows a node ... **priority arbitration** scheme that would not require any standard ...  
portal.acm.org/ft\_gateway.cfm?id=227255&type=pdf - [Similar pages](#)

[PDF] [Data-Driven and Demand-Driven Computer Architecture](#)

File Format: PDF/Adobe Acrobat

**counter** is updated at the end of each **cycle** ... to **access shared memory** cells. This form. of data sharing is. **shared** update., in which ...

portal.acm.org/ft\_gateway.cfm?id=356873&

type=pdf&coll=ACM&dl=ACM&CFID=15151515&CFTOKE... - [Similar pages](#)

[PDF] [Cache Memories INTRODUCTION](#)

File Format: PDF/Adobe Acrobat

percent of the time required to **access** main **memory**, cache memories permit the ...  
2.13.2 **Priority Arbitration**. An issue related to cache bandwidth is what ...

portal.acm.org/affiliated/ft\_gateway.cfm?id=356892&type=pdf&coll=ACM&dl=ACM -

[Similar pages](#)

[PDF] [Tolerating Latency in Multiprocessors through Compiler-Inserted ...](#)

File Format: PDF/Adobe Acrobat

hierarchy to reduce the effective **memory access** time. The **memory** hierar-. chy of a **shared-memory** multiprocessor can be further optimized by (1) ...

portal.acm.org/ft\_gateway.cfm?id=273021&

type=pdf&coll=ACM&dl=ACM&CFID=15151515&CFTOKE... - [Similar pages](#)

[PDF] [A Design Flow for Partially Reconfigurable Hardware](#)

File Format: PDF/Adobe Acrobat

interface allows dynamic tasks **access** to the system **memory** to save and recover ...  
**priority** level is required. r. Change the **priority arbitration** algorithm. ...

portal.acm.org/affiliated/ft\_gateway.cfm?id=993399&

type=pdf&coll=ACM&dl=ACM&CFID=15151515... - [Similar pages](#)

[PDF] [Virtual Multiprocessor: An Analyzable, High-Performance ...](#)

File Format: PDF/Adobe Acrobat

cally compete for **shared** processor resources. To sum up, SMT is ... **counter** decrements by one each **cycle**. When the watchdog **counter** ...

portal.acm.org/ft\_gateway.cfm?id=1086326&

type=pdf&coll=GUIDE&dl=ACM&CFID=15151515&CFT... - [Similar pages](#)

[PDF] [A Hardware-Driven Profiling Scheme for Identifying Program Hot ...](#)

File Format: PDF/Adobe Acrobat

Additionally, code that yields a sig-. nificant short-term benefit is given **priority** since a run-. time optimizer may not have enough **memory** at its ...

portal.acm.org/affiliated/ft\_gateway.cfm?id=300991&type=pdf&coll=ACM&dl=ACM -

[Similar pages](#)

[PDF] [Rotating Combined Queueing \(RCQ\):](#)

File Format: PDF/Adobe Acrobat

one clock **cycle** per **priority**. comparison. It is assumed ... in random-**access**. **memory**. (RAM). buffer., each queue needs a header. and a tail pointer. ...

portal.acm.org/ft\_gateway.cfm?id=232996&type=pdf - [Similar pages](#)

[Communicating sequential processes](#)

Iyad Ouaiss , Ranga Vemuri, Efficient resource **arbitration** in ... Régis Cridlig, Semantic analysis of **shared-memory** concurrent languages using abstract ...

portal.acm.org/citation.cfm?id=359585&

dl=ACM&coll=portal&CFID=901287&CFTOKEN=19619918 - [Similar pages](#)

[PDF] [Ariadne\\*— An Adaptive Router for Fault-tolerant Multicomputers](#)

File Format: PDF/Adobe Acrobat

**priority**. support. While. multiple. modes. are supported, ... **cycle**. times. represents. the period. of the **arbitration**. oscillator. with. **arbitration** ...

portal.acm.org/affiliated/ft\_gateway.cfm?id=192040&type=pdf&dl=ACM&dl=ACM -

[Similar pages](#)

[PDF] [Analysis and Implementation of Hybrid Switching](#)

File Format: PDF/Adobe Acrobat

**Access**. to the. bus is regu-. lated. by a binary. **priority-tree**. arbiter. [13, 14]. ... to free pages in the **memory**., for storing. arriving. packets. ...

portal.acm.org/ft\_gateway.cfm?id=224432&type=pdf - [Similar pages](#)

[PDF] [Computing Curricula 2001 Computer Science](#)

File Format: PDF/Adobe Acrobat

preemptive scheduling of tasks in operating systems, such as **priority**, ... bus protocols, **arbitration**, direct-**memory access** (DMA); introduction to networks; ...

portal.acm.org/ft\_gateway.cfm?id=384275&type=pdf&coll=GUIDE&dl=GUIDE -  
[Similar pages](#)

[PDF] [Analysis and Implementation of Hybrid Switching](#)

File Format: PDF/Adobe Acrobat

**Access.** to the bus is regulated by a binary **priority-tree** arbiter. [13, 14]. ... per **cycle**). as a function. of the number. of hops traveled ...

portal.acm.org/ft\_gateway.cfm?id=224432&  
type=pdf&coll=ACM&dl=ACM&CFID=15151515&CFTOKE... - [Similar pages](#)

[PDF] [SMC: A Symmetry-Based Model Checker for Verification of Safety and ...](#)

File Format: PDF/Adobe Acrobat

This involves an **arbitration** with. **IMMEDIATE priority**. The action LinkData.conf() on the sender ... When the state symmetry option is employed, the **memory** ...

portal.acm.org/ft\_gateway.cfm?id=350891&  
type=pdf&coll=GUIDE&dl=GUIDE&CFID=1194792&CFT... - [Similar pages](#)

[PDF] [Formal verification in hardware design: a survey](#)

File Format: PDF/Adobe Acrobat

**access** to a bus and they all have the same **priority**, the bus controller may ... The Sun S3.mp **shared-memory** multiprocessor [Nowatzky et al. 1995] ...

portal.acm.org/ft\_gateway.cfm?id=307989&  
type=pdf&coll=GUIDE&dl=ACM&CFID=15151515&CFTO... - [Similar pages](#)

[Communicating sequential processes](#)

George W. Cherry, Covering the life **cycle** with Ada: Ada all the way, ... Régis Cridlig, Semantic analysis of **shared-memory** concurrent languages using ...

portal.acm.org/affiliated/citation.cfm?id=359585&  
dl=ACM&coll=ACM&CFID=577211&CFTOKEN=45296410 - [Similar pages](#)

[PDF] [Loose Synchronization of Event-Triggered Networks for Distribution ...](#)

File Format: PDF/Adobe Acrobat

of the **cycle** is called the receptive interval. For nonsynchronizing ... [9], for the case of a 1Mbit/s CAN bus, most high-**priority** mes- ...

portal.acm.org/ft\_gateway.cfm?id=1017786&  
type=pdf&coll=ACM&dl=ACM&CFID=15151515&CFTOK... - [Similar pages](#)

*In order to show you the most relevant results, we have omitted some entries very similar to the 84 already displayed. If you like, you can repeat the search with the omitted results included.*

See all your photos right in your Sidebar.  
Free! [Download Google Desktop](#).

site:portal.acm.org priority counter arbitration access cycle memory shared -

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2006 Google